

12/21/00
Jc962 U.S.
Patent Application of:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Martin Ceredig Roberts et al.

**METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL P
PROCESS**

12/21/00
Jc962 U.S. PTO
745780 A

Attorney Docket No.: 303.451US6

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

Return postcard.

CONTINUATION of prior Patent Application No. 09/351,884 (under 37 CFR § 1.53(b)) comprising:

- Specification (20 pgs, including claims numbered 1 through 33 and a 1 page Abstract).
- Formal Drawing(s) (9 sheets).
- Copy of signed Declaration and Power of Attorney (6 pgs) from prior application.
- Copy of Revocation and Power of Attorney from prior application (1 pg.)
- Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
- Check in the amount of \$1,990.00 to pay the filing fee.

Prior application is assigned of record to Micron Technology, Inc.

Information Disclosure Statement (1 pgs), Form 1449 (1 pgs). References NOT enclosed, cited in prior application.

Preliminary Amendment (10 pgs).

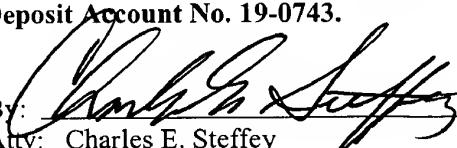
A Communication Re: Change of Correspondence Address (1 pg.).

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	19 - 20 =	0	x 18 =	\$0.00
INDEPENDENT CLAIMS	19 - 3 =	16	x 80 =	\$1,280.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$710.00
				\$1,990.00

Please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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Date of Deposit: December 21, 2000

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.

Serial Number: 08/390,714

Filing Date: February 17, 1995

Title: METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS

CERTIFICATE UNDER 37 CFR §3.73(b)

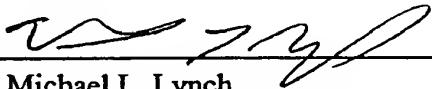
Micron Technology, Inc. hereby certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor filed March 20, 1995 and recorded on Reel 7400, Frames 0824 - NA. To the best of my knowledge and belief, title is in the assignee, Micron Technology, Inc..

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Michael L. Lynch, am empowered to sign this certificate on behalf of the assignee, Micron Technology, Inc..

I hereby declare that all statement made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true.

Please direct all correspondence in this case to:

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Date October 16, 1997 By 
Michael L. Lynch
Title: Chief Patent Counsel